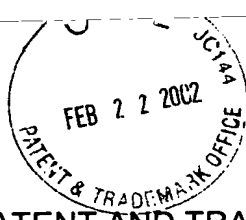


Docket No.: W&B-INF-952



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Robert Kaiser et al.
Applic. No. : 10/034,070
Filed : December 20, 2001
Title : Integrated Circuit Having a Data Processing Unit and a Buffer Memory

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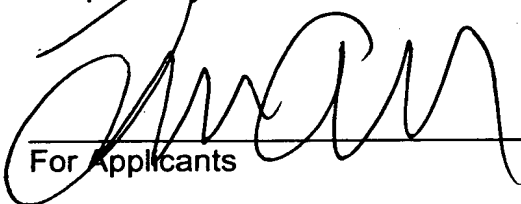
Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

Please recognize MARKUS NOLFF (Reg. No. 37,006) as my associate in the matter in the above-identified application, with full powers. Please continue addressing all communications to the following address:

Lerner and Greenberg, P.A.
P.O. Box 2480
Hollywood, Florida 33022-2480

Respectfully submitted,


For Applicants

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/kf